

CORE, MEMORIES AND ACCELERATION - Single-core Cortex-M7 up to 550 MHz - Dual-core Cortex-M7 480 MHz and Cortex-M4 240 MHz - Flash and RAM acceleration - SP-FPU and DP-FPU 4 x DMA - Mathematics (only H723/733/725/735/730) CONNECTIVITY - Up to 2 x USB2.0 OTG FS/HS 2 x SDMMC - USART, UART, SPI, I²C - Up to 3 x CAN (2 x FD and 1 x TT) - HDMI-CEC - FMC, Dual-mode Quad-SPI or 2 x Octo-SPI - Camera I/F AUDIO 3 x I²S + audio PLL 4 x SAI 2 x 12-bit DAC - SPDIF-RX GRAPHIC - Chrom-ART Accelerator™ OTHER - Crypto/Hash option (except H742)¹ - Security services option (except H742) - TRNG - DFSDM 16- and 32-bit timers - HRTimer (except STM32H7A/H7B/H7B0/H723/H725/H730/H733/H735) - Up to 3 x 16-bit ADC (up to 3.6 MSPS) - Analog (compt,A0P) - Voltage range 1.62 to 3.6 V (except 100-pin and VFQFPN68 packages : 1.71 to 3.6 V) - Multi-power domains -40°C up to 105°C ambient -40°C up to 125°C ambient²	Product line	F _{CPU} (MHz)	Dual-Bank Flash memory (bytes)	RAM (bytes)	OctoSPI & OTFDEC ³	Ethernet	Graphic	Power supply	Stop mode (typical) / RAM retention
	Dual-core lines								
	STM32H747/757 ¹	480 + 240	Up to 2 Mbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup ¹) + 4 Kbytes backup ²		•	TFT-LCD JPEG codec MIPI-DSI	SMPS + LDO	360 µA / 1MB 250 µA / 768KB
	STM32H745/755 ¹	480 + 240	Up to 2 Mbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup ¹) + 4 Kbytes backup ²		•	TFT-LCD JPEG codec	SMPS + LDO	360 µA / 1MB 250 µA / 768KB
	Single-core lines								
	STM32H7A3/7B3 ¹	280	Up to 2 Mbytes	1,4MB (incl.128K DTCM, 64K ITCM, 1184K+SRAM, 4K backup)	•		TFT-LCD JPEG codec Chrom-GRC	SMPS + LDO	32 µA / 1.4MB 28 µA / 32KB
	STM32H743/753 ¹	480	Up to 2 Mbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup ¹) + 4 Kbytes backup ²		•	TFT-LCD JPEG codec	LDO	1270 µA / 1MB 910 µA / 768KB
	STM32H742	480	Up to 2 Mbytes	692 Kbytes (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 16 Kbytes backup ¹) + 4 Kbytes backup ²		•		LDO	1270 µA / 692KB 910 µA / 704KB
	STM32H725/735 ³	550	Up to 1 Mbyte	564KB (incl.128K DTCM, 432KB Syst + 4K bckup)	•	•	TFT-LCD	SMPS ⁵ + LDO	200 µA / 564KB
	STM32H723/733 ³	550	Up to 1 Mbyte	564KB (incl.128K DTCM, 432KB Syst + 4K bckup)	•	•	TFT-LCD	LDO	520 µA / 564KB
	Value line								
	STM32H7B0	280	128 Kbytes	1,4MB (incl.128K DTCM, 64K ITCM, 1184K+SRAM, 4K backup)	•		TFT-LCD JPEG codec Chrom-GRC	SMPS + LDO	32 µA / 1.4MB 28 µA / 32KB
	STM32H750	480	128 Kbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup ¹) + 4 Kbytes backup ²		•	TFT-LCD JPEG codec	LDO	1270 µA / 1MB 910 µA / 768KB
	STM32H730	550	128 Kbytes	564KB (incl.128K DTCM, 432KB Syst + 4K bckup)	•	•	TFT-LCD	SMPS ⁴ + LDO	200 µA / 564KB 520 µA / 564KB

Notes:

- Optional - dedicated CPN, STM32H733, STM32H735, STM32H753, STM32H755, STM32H757, STM32H7B3 for the Crypto Variants
- 125 °C ambient / 140 °C junction. Dedicated part numbers on STM32H725/H735, STM32H745/H755
- Crypto and Security services on CPN : STM32H733, STM32H735 and STM32H730
- SMPS available only on STM32H730Q CPN
- SMPS only on the QFN68 variant (no LDO)

STM32 Ecosystem

STM32Cube	Evaluation tools	Software tools	Embedded Software	Hardware tools	Security	MadeFor STM32	ST Partners
							

STM32 Solutions

Artificial Neural Networks	Audio/Voice	Connectivity	Digital Power	Graphical User Interface	Motor Control	Safety
						

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