



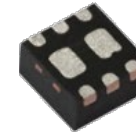
COMPLEMENTARY N+P IN COMPACT FOOTPRINT

Package Family

PowerPAK®

LITTLE FOOT®

Package Isometric Image



V _{DS} (V)		V _{GS} (V)		R _{DS(ON)} Max (mΩ) @ V _{GS} =								ESD	SC-70	1206-8 ChipFET®
P	N	P	N	4.5V		2.5V		1.8V		1.5V			2 x 2	3 x 1.9
-20	12	8	8	54	28	70	33	104	42	165		✓	SiA537EDJ	
-12	12	8	8	41	29	60	34	110	44	174	65		SiA527DJ	
-12	12	8	8	61	29	81	34	115	44	170	65		SiA517DJ	
-12	12	8	8	59	34	81	40	115	50	215	70	✓	SiA533EDJ	
-20	20	12	12	90	40	137	65					✓	SiA519EDJ	
-20	20	8	8	100	36	120	41	156	50					Si5515CDC

Features and Benefits to Design:

- ✓ Very low R_{DS(ON)} provides flexible and efficient solution for synchronous buck or boost DC/DC
- ✓ R_{DS(ON)} is rated down to V_{GS} = 1.5V – allow further design flexibility, operable in low battery condition
- ✓ ESD protection on selective devices – prevent damages from handling or static