

Evaluates: ADPL86612 4.5V to 60V, 250mA, Current-Limiter with Reverse Protection

General Description

The ADPL86612 evaluation kit (EV kit) is a fully assembled and tested circuit board that demonstrates the ADPL86612 4.5V to 60V, 250mA, current-limiter with reverse protection in a 10-pin Thin dual flat no lead-exposed pad (TDFN-EP) package. The EV kit can be configured to demonstrate different current-limit types, and different current-limit thresholds.

Features

- 4.5V to 60V Operating-Voltage Range
- Features a Transient-Voltage-Suppression (TVS) Diode across the Input and a Schottky Diode across the Output Terminals
- Evaluates three Current-Limit Types, and Current-Limit Threshold
- Internal Undervoltage-Lockout (UVLO) set to 4.2V
- Jumper-Configurable Current-Limit (Selected as 250mA by Default)
- Current-Limit Mode Set To Autoretry by Default
- Proven PCB Layout
- Fully Assembled and Tested

[Ordering Information](#) appears at end of data sheet.

ADPL86612 Evaluation Kit

Quick Start

Recommended Equipment

- ADPL86612 EV kit
- 60V DC power supply
- Multimeters
- Adjustable load (0A to 1A)
- USB-A male to USB-B male cable or 5V DC power supply

Equipment Setup and Test Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation.

Caution: Do not turn on the power supply until all connections are completed.

- 1) Verify that all jumpers are in their default positions.
- 2) Connect the USB cable to J1 from a computer or connect a 5V-DC power supply to TP3.
- 3) Verify that LED1 is on.
- 4) Set the 60V DC power supply to 5V and connect to IN (J2/TP6). Verify that OUT (J3/TP8) is 5V.
- 5) Set the DC power-supply voltage to 24V and connect the adjustable load between the OUT and GND terminals and a multimeter in series to measure the current. Gradually increase the load current and verify that the OUT goes down and $\overline{\text{FWD}}$ goes low when the load current increases above 250mA.
- 6) The jumper JU1 can be configured to change the current limit as given in [Table 2](#). Verify various current limit operations by repeating step 5.

CAUTION: When applying a negative input to V_{IN} , the negative input test should be performed when the output capacitors are fully discharged and V_{BUS} is not supplied.

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Detailed Description

The EV kit circuit can be configured to evaluate the overcurrent threshold by external resistors connected to the SET1 pin and is jumper-configurable through jumper JU1. Using jumper JU4, the EV kit circuit can be configured to evaluate different current limit types (Autoretry, Continuous, and Latch-off). LED1 on the EV kit indicates the availability of logic power for annunciation signals (FWD and REV) and EN.

The EV kit provides on-board output capacitors to enable a demonstration of the ADPL86612 protection features.

Input-Power Supply

The EV kit is powered by a user-supplied 4.5V to 60V power supply connected between J2/TP6 (INPUT POWER) and GND.

Enable

To enable the device, connect a USB-A male connector from the computer to the USB-B female connector, J1, or

an external 5V supply to TP3 and GND. This provides 5V to V_{BUS} and to the EN pin (JU5 connects V_{BUS} to EN by default). Choose the JU5 setting to enable or disable operation of the ADPL86612 (see [Table 1](#)).

Current-Limit Threshold

The EV kit features a jumper (JU1) to select the current-limit threshold. Install a jumper as shown in [Table 2](#) to change the current-limit threshold.

Current-Limit Type Select

The EV kit features jumper JU4 to select different current-limit responses. See [Table 3](#) for jumper settings.

Output-Load Capacitor

Use JU6 to connect the OUT pins to the OUT test point (TP8). Use jumper JU7 to connect the output to the 330 μ F capacitors. See [Table 4](#) for jumper settings

Table 1. Enable (JU5)

JUMPER	SHUNT POSITION	DESCRIPTION	ADPL86612 STATUS
JU5	1 to 2*	EN pin connected to V_{BUS}	ON
	2 to 3	EN pin connected to GND	OFF
	Open	EN pin floating	ON

*Default position.

Table 2. Current-Limit Threshold (JU1)

JUMPER	SHUNT POSITION	DESCRIPTION
JU1	1 to 2	Current limit 10mA
	3 to 4	Current limit 100mA
	5 to 6*	Current limit 250mA
	7 to 8	Current limit adjustable

*Default position.

Table 3. Current-Limit Type Select (JU4)

JUMPER	SHUNT POSITION	DESCRIPTION
JU4	1 to 2	Latch-off
	2 to 3	Continuous
	Open*	Autoretry

*Default position.

Table 4. Output Load Capacitor (JU7)

JUMPER	SHUNT POSITION	DESCRIPTION
JU7	Installed	OUT connected to C4 and C5.
	Not installed*	OUT not connected to C4 and C5.

*Default position.

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ADPL86612 EV Kit Performance Report

($V_{IN} = 24V$, $C_{IN} = 0.47\mu F$, $C_{OUT} = 4.7\mu F$, unless otherwise noted.)

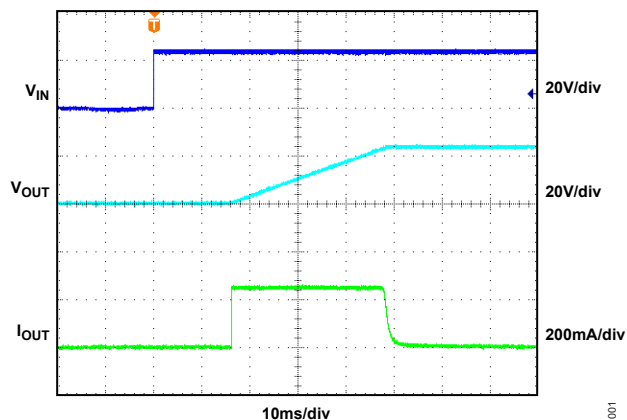


Figure 1. Power-Up Response with 330 μF Capacitor at No Load

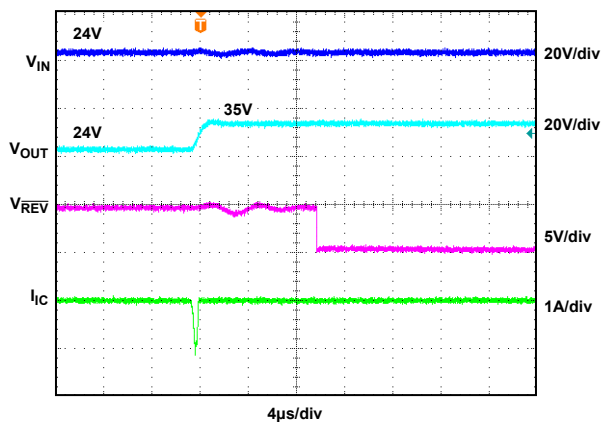


Figure 2. Reverse-Blocking Response

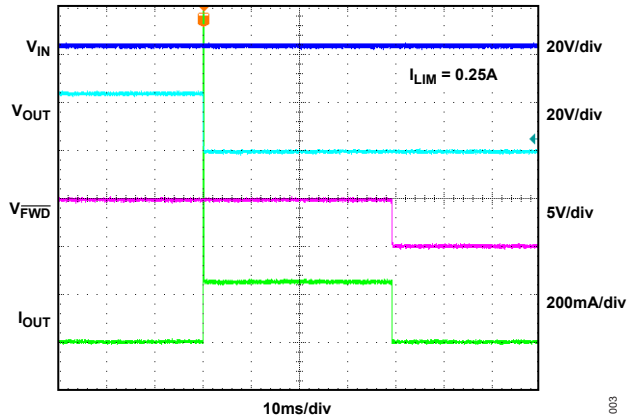


Figure 3. Output Short Circuit Response

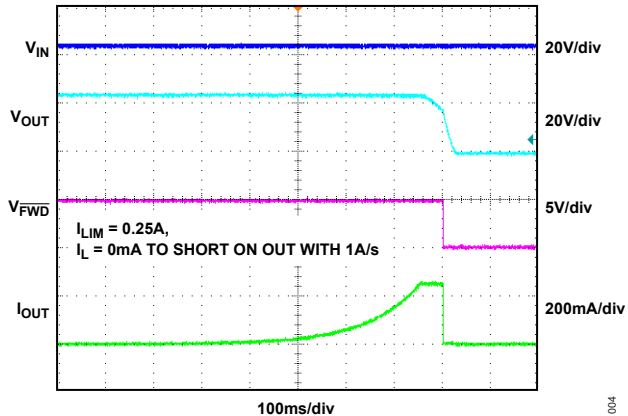


Figure 4. Current Limit Response

ADPL86612 EV Kit Performance Report (continued)

($V_{IN} = 24V$, $C_{IN} = 0.47\mu F$, $C_{OUT} = 4.7\mu F$, unless otherwise noted.)

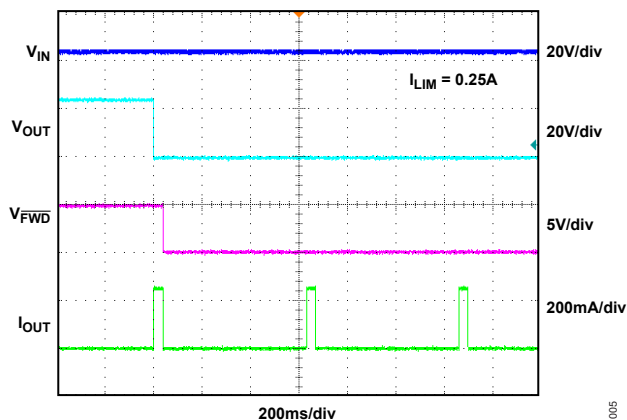


Figure 5. Autoretry Current-Limit Response

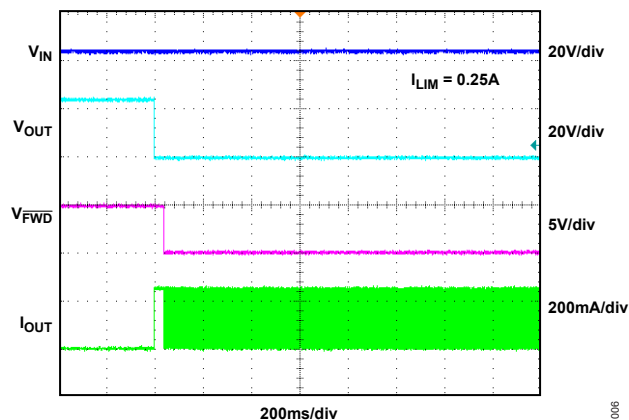


Figure 6. Continuous Current Limit Response

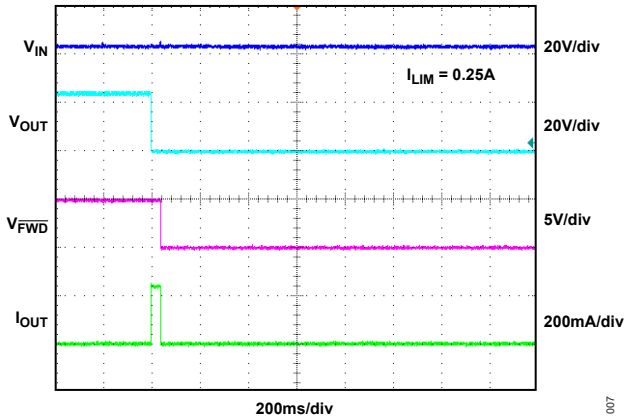


Figure 7. Latch-Off Current Limit Response

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Ordering Information

PART	TYPE
ADPL86612EVKIT#	EV Kit

#Denotes RoHS-compliant

ADPL86612 EV Kit Bill of Materials

PART REFERENCE	QTY	DESCRIPTION	MANUFACTURER PART NUMBER
C1	1	1 μ F 10%, 50V X5R ceramic capacitors (0603)	TDK C1608X5R1H105K080AB, Samsung CL10A105KB8NNNC
C2	1	0.47 μ F 10%, 100V X7R ceramic capacitors (0805)	Murata GRM21BR72A474KA73L
C3	1	4.7 μ F 10%, 100V X7R ceramic capacitors (1210)	Kemet C1210C475K1R2C, Murata GRM32ER72A475KE14
C4	1	330 μ F 20%, 50V aluminium (10mm)	Panasonic EEU-EB1H331
D1	1	TVS Diode, 600W (SMB)	Bourns SMBJ40CA
D2	1	Power Schottky Diode, 60V, 5A (SMC)	Diodes Incorporated B560CQ-13-F
D3	1	Power Schottky Diode, 60V, 1A (SMA)	Diodes Incorporated B160-13-F
J1	1	USB-B connector	FCI Connect 61729-0010BLF
J2, J3	2	2-Pin Green PC Terminal Block	Degson Electronics DG128-5.0-02P-14
JU1	1	2x4 Dual-Row Header, 0.1in centers, cut to fit	Sullins Connector PBC04DAAN
JU3, JU6, JU7	3	2-Pin Single-Row Header, 0.1in centers, cut to fit	Sullins Connector PEC02SAAN
JU4, JU5	2	3-Pin Single-Row Header, 0.1in centers, cut to fit	Sullins Connector PEC03SAAN
LED1	1	Green LED (1206)	Kingbright APT3216SGC
R1	1	1k Ω 1% resistors (0603)	—
R2, R3	2	10k Ω 1% resistors (0402)	—
R4	1	150k Ω 5% resistor (0402)	—
R5, R13	2	5k Ω 0.1% resistors (0402)	—
R6	1	30k Ω 1% resistors (0402)	—
R7	1	3k Ω 1% resistors (0402)	—
R8, R15	2	1.2k Ω 1% resistors (0402)	—
R14	1	20k Ω 1% resistors (0402)	—
R16	1	25k Ω Trimmer Potentiometers	Bourns 3296Y-1-253LF
TP1, TP9, TP11	3	White Test Point	Keystone Electronics Corp. 5002
TP2, TP4, TP5, TP7	4	Black Test Point	Keystone Electronics Corp. 5001
TP3, TP6, TP8	3	Red Test Point	Keystone Electronics Corp. 5000
TP10	1	Green Test Point	Keystone Electronics Corp. 5116
U1	1	4.5V to 60V, 250mA, Current-Limiter with Reverse Protection (10-pin TDFN-EP, 3mm x 3mm)	ADPL86612ATB+
C5	0	Not Installed; 330 μ F 20%, 50V aluminium (10mm)	Panasonic EEU-EB1H331
D4	0	Not Installed; TVS Diode, 600W (SMB)	Bourns SMBJ40CA
PCB	1	PCB: ADPL86612 Evaluation Kit	—

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ADPL86612 EV Kit Schematic Diagram

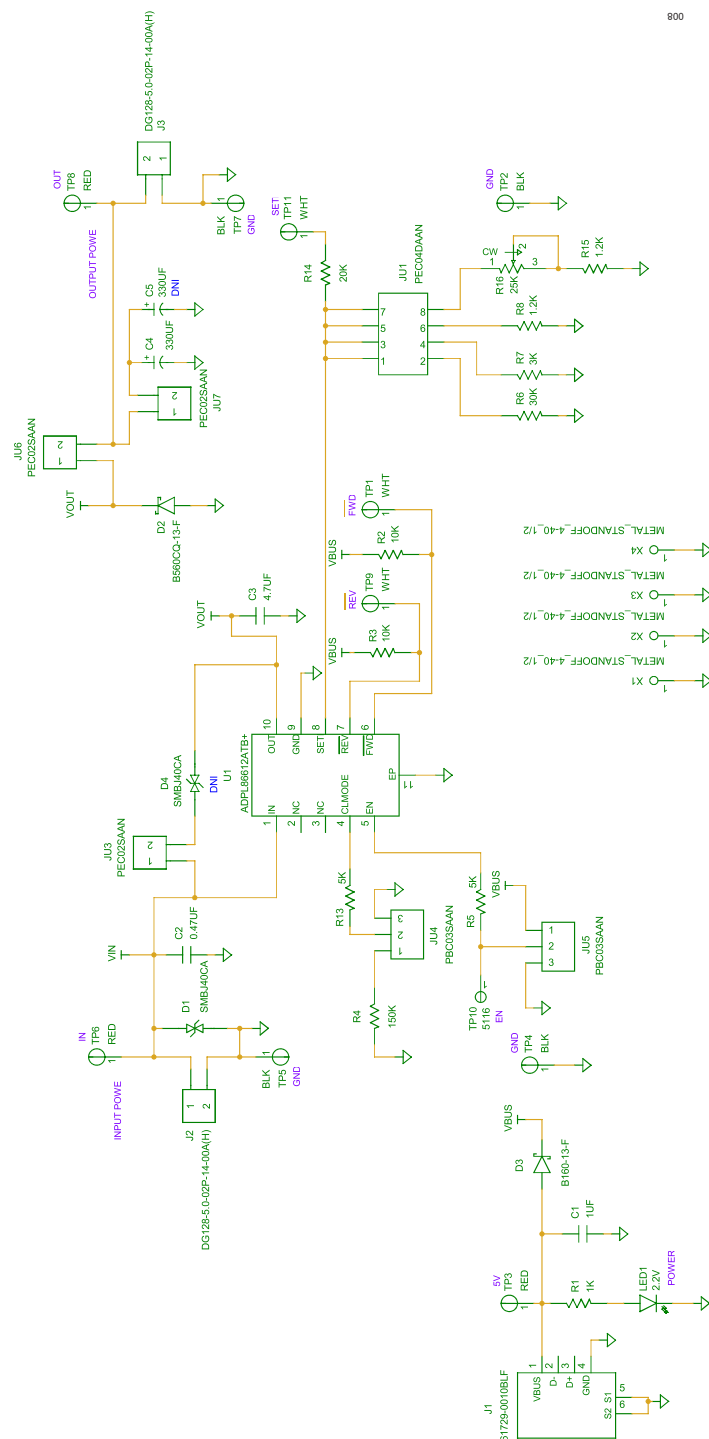


Figure 8. ADPL86612 EV kit Schematic Diagram

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ADPL86612 EV Kit PCB Layout Diagrams

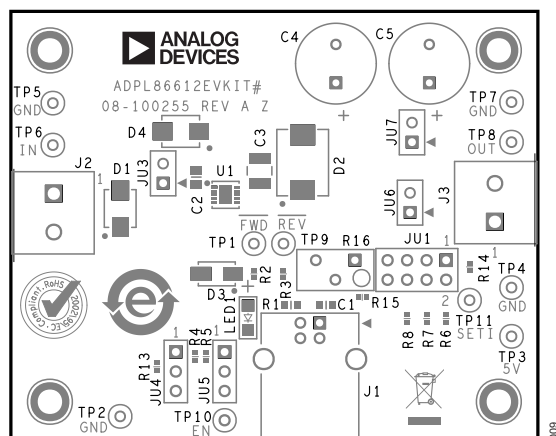


Figure 9. ADPL86612 EV Kit PCB Layout—Top Silkscreen

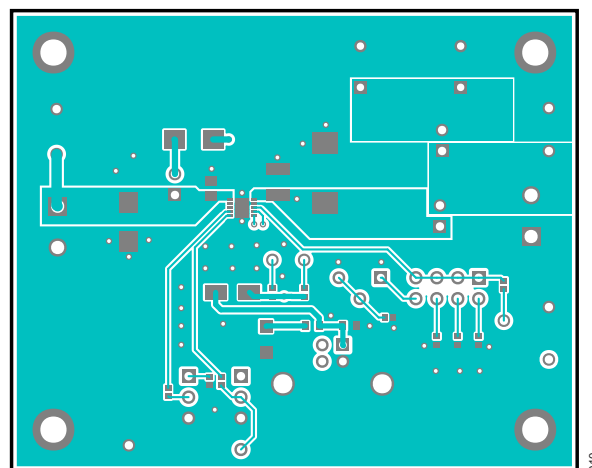


Figure 10. ADPL86612 EV Kit PCB Layout—Top Layer

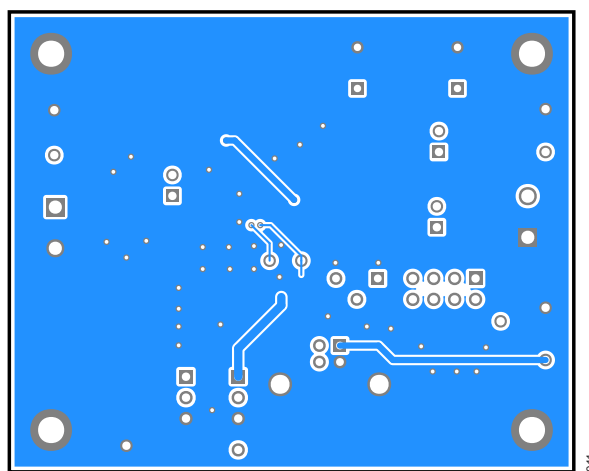


Figure 11. ADPL86612 EV Kit PCB Layout—Bottom Layer

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	10/25	Initial release	—

