

SPECIAL FEATURES				
Resistance Values	≥ 0.010	≥ 0.045	≥ 0.1	≥ 0.5
Tolerances	± 1 % at ± 10 %			
Requirement Temperature Coefficient (TCR) (-55 °C +150 °C) IEC 60115-1	± 1100 ppm/°C	± 700 ppm/°C	± 250 ppm/°C	± 150 ppm/°C

PERFORMANCE		
TESTS	CONDITIONS	REQUIREMENTS
Momentary Overload	IEC 60115-1 §4.13 2 Pr 5 s for $R < 2 \Omega$ 1.5 Pr 5 s for $R \geq 2 \Omega$ US < 1.5 UL	± (0.25 % + 0.005 Ω)
Load Life	IEC 60115-1 1000 h, 90/30 Pr at +25 °C	± (0.5 % + 0.005 Ω)
High Temperature Exposure	AEC-Q200 rev. D conditions: MIL-STD-202 method 108 1000 h, +175 °C, unpowered	± (0.25 % + 0.005 Ω)
Temperature Cycling	Pre-conditioning 3 reflows according JESTD020D IEC 60068-2-14 test Na 1000 cycles, -55 °C/+175 °C Dwell time - 15 min	± (0.5 % + 0.005 Ω)
Moisture Resistance	AEC-Q200 rev. D conditions: MIL-STD-202 method 106 10 cycles, 24 h, unpowered	± (0.5 % + 0.005 Ω)
Biased Humidity	AEC-Q200 rev. D conditions: MIL-STD-202 method 103 1000 h, 85°C, 85 % RH	± (0.5 % + 0.005 Ω)
Operational Life	AEC-Q200 rev. D conditions: Pre-conditioning 3 reflows according JESTD020D MIL-STD-202 method 108 2000 h, 90/30, powered, +125 °C	± (0.5 % + 0.005 Ω)
ESD Human Body Model	AEC-Q200 rev. D conditions: AEC-Q200-002 25 kV _{AD}	± (0.5 % + 0.005 Ω)
Vibration	AEC-Q200 rev. D conditions: MIL-STD-202 method 204 5 g's for 20 min, 12 cycles test from 10 Hz to 2000 Hz	± (0.2 % + 0.005 Ω)
Mechanical Shock	AEC-Q200 rev. D conditions: MIL-STD-202 method 213 100 g's, 6 ms, 3.75 m/s 3 shocks/direction	± (0.2 % + 0.005 Ω)
Board Flex	AEC-Q200 rev. D conditions: AEC-Q200-005 Bending 2 mm/60 s	± (0.25 % + 0.01 Ω)
Terminal Strength	AEC-Q200 rev. D conditions: AEC-Q200-006 1.8 kgf/60 s	± (0.25 % + 0.01 Ω)

ASSEMBLY SPECIFICATIONS		
For the assembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C		
TESTS	CONDITIONS	REQUIREMENTS
Resistance to Soldering Heat	IEC 60115-1 IEC 60068-2-58 Solder bath method: 270 °C/10 s	± (0.5 % + 0.005 Ω)
Moisture Sensitivity Level (MSL)	IPC/JEDEC® J-STD-020C 85 °C / 85 % RH / 168 h	Level: 1 + pass requirements of TCR overload and dielectric strength after MSL

CHOICE OF THE BOARD

The user must choose the board according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 175 °C. The dissipated power is simply calculated by the following ratio:

$$P = \frac{\Delta T}{R_{TH}(j-c) + R_{TH}(c-h) + R_{TH}(h-a)} \quad (1)$$

P: expressed in W

ΔT: difference between maximum working temperature and room temperature or fluid cooling temperature

$R_{TH}(j-c)$: thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component: 6.5 °C/W.

$R_{TH}(c-h)$: thermal resistance value measured between outer side of the resistor and upper side of the board. This is the thermal resistance of the solder layer.

$R_{TH}(h-a)$: thermal resistance of the board.

Example:

$R_{TH}(c-h) + R_{TH}(h-a)$ for D2TO20 power rating 2.5 W at ambient temperature +25 °C.

Thermal resistance $R_{TH}(j-c)$: 6.5 °C/W

Considering equation (1) we have:

$$\Delta T = 155 \text{ °C} - 25 \text{ °C} = 130 \text{ °C}$$

$$R_{TH}(j-c) + R_{TH}(c-h) + R_{TH}(h-a) = \Delta T/P = 130/2.5 = 52 \text{ °C/W}$$

$$R_{TH}(c-h) + R_{TH}(h-a) = 52 \text{ °C/W} - 6.5 \text{ °C/W} = 45.5 \text{ °C/W}$$

Single Pulse:

These informations are for a single pulse on a cold resistor at 25 °C (not already used for a dissipation) and for pulses of 100 ms maximum duration.

The formula used to calculate E is:

$$E = P \times t = \frac{U^2}{R} \times t$$

with:

E (J): pulse energy

P (W): pulse power

t (s): pulse duration

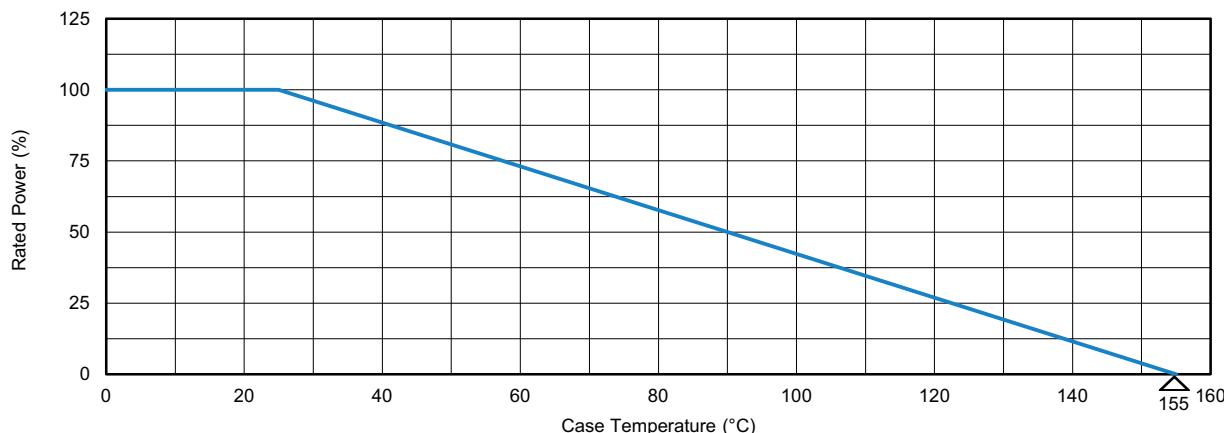
U (V): pulse voltage

R (Ω): resistor

The energy calculated must be less than that allowed by the graph.

POWER RATING

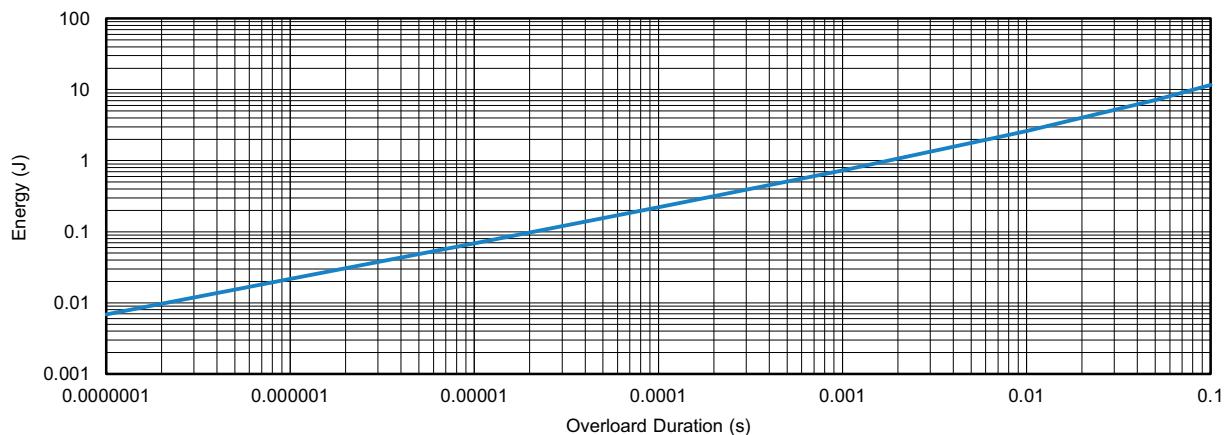
The temperature of the case should be maintained within the limits specified.



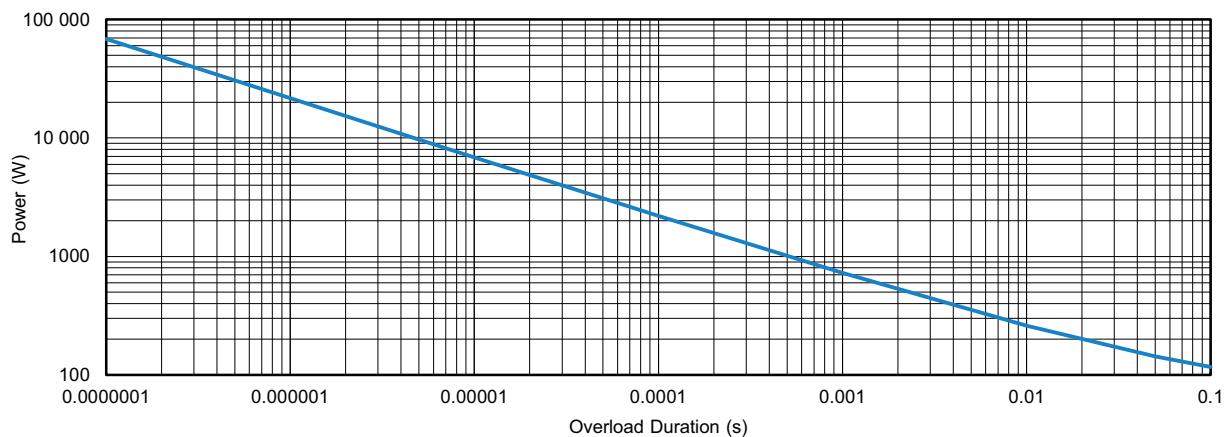
OVERLOADS

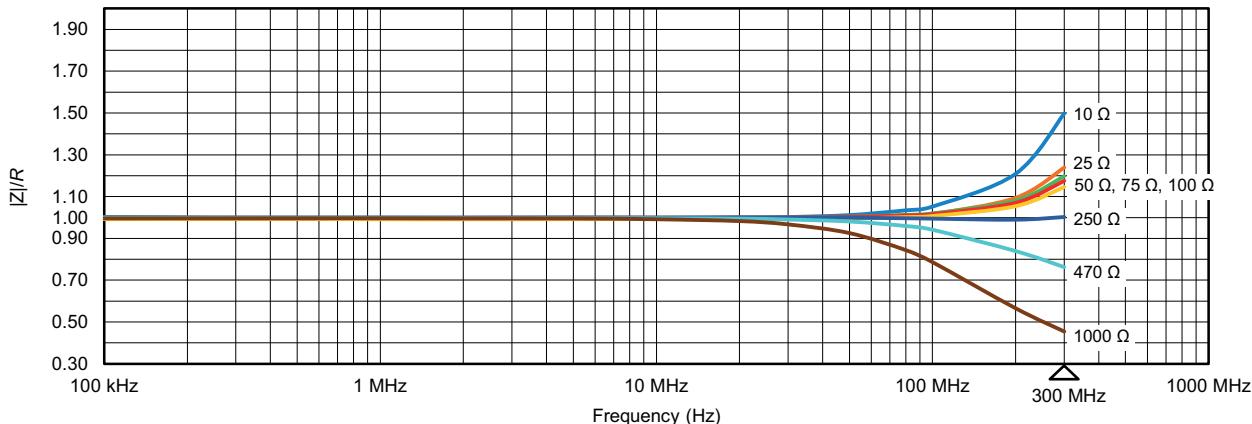
In any case the applied voltage must be lower than the maximum overload voltage of 750 V. The values indicated on the graph below are applicable to resistors in air or mounted onto a board.

ENERGY CURVE

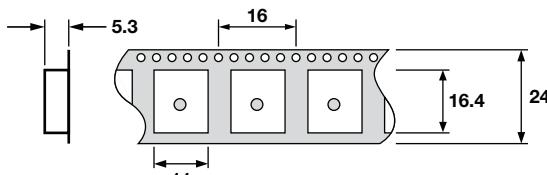


POWER CURVE



IMPEDANCE CURVE 10 Ω to 1 k Ω from 100 kHz to 300 MHz

PACKAGING

- Reel
- Tube
- Tape dimensions (mm) for reel:


MARKING

Model, style, resistance value (in Ω), tolerance (in %), manufacturing date, Vishay Sfernice trademark

ORDERING INFORMATION

D2TO	020	C	100 K Ω	$\pm 1\%$	XXX	e3
MODEL	STYLE	CONNECTIONS	RESISTANCE VALUE	TOLERANCE	CUSTOM DESIGN	LEAD (Pb)-FREE
				F = $\pm 1\%$ G = $\pm 2\%$ J = $\pm 5\%$ K = $\pm 10\%$	Optional on request: shape, etc.	

SAP PART NUMBERING GUIDELINES

D	2	T	O	0	2	0	C	R	2	0	0	0	K	R	E	3
GLOBAL MODEL	SIZE	LEADS	OHMIC VALUE				TOLERANCE	PACKAGING				LEAD (Pb)-FREE				
D2TO	020	C = surface-mount	The first four digits are significant figures and the last digit specifies the number of zeros to follow. R designates decimal point. 48R70 = 48.7 Ω 48701 = 48 700 Ω 10002 = 100 000 Ω R0100 = 0.01 Ω R6800 = 0.68 Ω 27000 = 2700 Ω = 2.7 k Ω				F = 1 % G = 2 % J = 5 % K = 10 %	R = reel 500 pieces T = tube 50 pieces				E3 = pure tin				

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